

Register designations are as follows:

Name	#	Name	#
pc = 0	(prog counter)	amask = 8	(fixed address mask = 4095)
ac = 1	(accumulator)	smask = 9	(fixed stack mask = 255)
sp = 2	(stack ptr)	a = 10	(a scratch)
ir = 3	(instr reg)	b = 11	(b scratch)
tir = 4	(tmp inst reg)	c = 12	(c scratch)
zr = 5	(fixed zero 0)	d = 13	(d scratch)
po = 6	(fixed plus 1)	e = 14	(e scratch)
no = 7	(fixed minus 1 (-1))	f = 15	(f scratch)

	MAL	VALID ?	A M U X	C O N D	A L U	S H	M B R	M A R	R D	W R	E N C	C	B	A	ADDR
A															
B															
C															
D															
E															

AMUX
0 = A latch
1 = MBR

COND
0 = no jmp
1 = jmp if n=1
2 = jmp if z=1
3 = always jmp

ALU
0 = A + B
1 = A and B
2 = A
3 = not A

SH
0 = no shift
1 = shift rt
2 = shift lt

MBR, MAR, RD, WR, ENC
0 = disable
1 = enable