

91.305 Computer Architecture Spring 2012 Class Schedule

Jan

- 23 M ch 2, Integer Representation, read ch2.4 to end ch 2
 25 W ch 2, Floating Point Representation, on-line floating point slides,
read Assign #1
 27 F on-line floating point slides, review Assign #1
 30 M complete ch 2, read Mic1 handout

Feb

- 1 W Mic1 Machine Architecture, read Mic1 handout and examples
Assign #1 due, Assign #2 handed out, read Assign #2
 3 F Mic1 Assembly Level Programming, review Assign #2
 6 M Mic1 Microcode Instruction Architecture and examples
 8 W Mic1 Microcode Instruction Implementation, Using the **mcc** Tool
10 F Assign #2 due, Assign #3 handed out, read Assign #3
 13 M Mic1 Adding New Microcode Instructions, review Assign #3
 15 W Mic1 Adding New Microcode Instructions, examples
 17 F Mic1 Implementing new machine level instructions, and updating
 assembler support

20 M *** Presidents' Day NO CLASS**

22 W Mic1 Integrating all the pieces, **Assign #3 due, Assign #4**
handed out, read Assign #4
 24 F Mic1 Assign #4 review
 27 M Review for exam #1, ch 2 and Mic1 material
 29 W Review for exam #1 (cont'd), ch 2 and Mic1 material

Mar

- 2 F Final review for exam #1, begin ch 6, Storage Technology, read
 ch6 – 6.1
5 M EXAM #1, ch 2, Mic1 material ch 6, Locality, read ch6.2 – 6.3
 7 W ch 6, Layered Memory, read ch6.4.1 – 6.4.3
 9 F Mic1 UART, **Assign #5 handed out, read Assign #5**

12 – 16 *** SRING BREAK NO CLASS**

19 M Assign #4 due, assignment #5 discussed, ch 6 Cache
 Organizations, read ch6.4.4 – 6.4.7
 21 W Mic-1 UART organization and serial port programming,
 assignment #5 IO
 23 F Memory hierarchy and ch 6 material, review ch 6, read ch6.5

26 M Cache organizations ch 6 Cache, read ch6.6
28 W Finish ch 6 and cache models, cache examples
30 F Chapter 6 summary, ch 7 Linkers, read ch7.1-7.5

Apr

2 M Introduction to linkers, ch 7 Symbol Tables, read ch7.6, **Assign #5 due, Assign #6 handed out**
4 W Linkers, assignment #6 discussed, ch 7 Relocation, read 7.7
6 F Linker relocation, assignment #6 details, ch 7 Executables, read ch7.8-7.9
9 M assignment #6 code examples , ch 7 Executables, read ch7.10
11 W Dynamic Linking, ch 7 Shared Libraries, read 7.11
13 F Review for exam #2, see exam 2 help file on-line
16 M ***** **Patriots' Day NO CLASS**
18 W **EXAM #2, ch 6 and ch 7, and Mic1 IO**
20 F Review exam, Begin ch 8 Exceptional Control Flow, read ch8.1-8.2, **Assign #6 due, Assign #7 handed out,**
23 M Exceptions and Processes, assignment #7 details, ch 8 Processes and Threads, read ch8.3
25 W Assignment #7 programming examples, read ch8.4
27 F ***** **University Day NO CLASS**
30 M Process Attributes and Control, ch 8 Signals, read ch8.5

May

2 W Continue Signals, ch 8 Non-local Jumps, read ch8.6
4 F Finish Process Organization, ch 8, finish ch 8
7 M Last class day, Review for final, **Assign #7 due, Last chance to submit other assignments ... assignment submission cutoff at midnight**

FINAL DATE – Thursday, May 10, 8:00 AM – 11:00 AM, Olsen 402 – ch 2, 6, 7, 8 and Assign#1 through #7